

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Satoru AKIYAMA et al
Serial No.: Not Yet Assigned
Filed: On Even Date
For: SEMICONDUCTOR DEVICE

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §§1.97 & 1.98

MS: PATENT APPLICATIONS

March 3, 2004

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In the matter of the above-identified application, applicants are submitting herewith copies of all documents listed in the attached form equivalent to Form PTO-1449 for the Examiner's consideration.

This Information Disclosure Statement is being submitted with the new application.

To the extent the document listed on the attached form equivalent to Form PTO-1449 is not in the English language, the requirement of 37 CFR 1.98(a)(3) for a concise explanation of the relevance is satisfied by the discussion of this document in the specification and the submission of an English language Abstract for the single Japanese patent reference.

It is respectfully requested that this Information Disclosure Statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry, Stout & Kraus Deposit Account No. 01-2135 (Case:500.43581X00), and please credit any excess fees to such deposit account.

Respectfully submitted,
ANTONELLI, TERRY, STOUT & KRAUS LLP



Gregory E. Montone
Registration No. 28,141

GEM/dks
(703) 312-6600
Attachments

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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>			Complete if Known		
			Application Number	Not Yet Assigned	
			Filing Date	March 3, 2004	
			First Named Inventor	Satoru AKIYAMA	
			Art Unit		
Sheet	1	of	1	Examiner Name	
				Attorney Docket Number	500.43581X00

U.S. PATENT DOCUMENTS						
Examiner Initials ¹	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)				
		US- 2003/0033492	A1	02/13/2003	Akiyama et al	
		US- 5,530,828		06/25/1996	Kaki et al	
		US- 5,999,474		12/07/1999	Leung et al	
		US- 6,075,740		06/13/2000	Leung	
		US- 6,285,626	B2	09/04/2001	Mizuno et al	
		US- 6,487,135	B2	11/26/2002	Watanabe et al	
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FOREIGN PATENT DOCUMENTS							
Examiner Initials ¹	Cite No. ¹	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)					
		JP 7-093215		04/07/1995	Toshiba Corp.		AB

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Applicant's unique citation designation number (optional). ²See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³Enter Office that issued the document, by the two-letter code (WIPO Standard St.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

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Substitute for form 1449B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Complete if Known			
		Application Number	Not Yet Assigned		
		Filing Date	March 3, 2004		
		First Named Inventor	S. AKIYAMA et al		
		Group Art Unit			
Sheet	2	of	2	Examiner Name	
				Attorney Docket Number	500.43581X00

OTHER PRIOR ART—NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
		ATWOOD, Bryan et al, "SESO Memory: A CMOS compatible with high density embedded memory technology for mobile application," 2002 Symposium on VLSI Circuit Digest of Technical Papers, pp. 154-1555, (2000) USA.	
		LAI, Stefan et al, "OUM - A 180 nm nonvolatile memory cell element technology for stand alone and embedded applications," 2001 IEEE International Electron Devices Meeting Digest of Technical Papers, pp. 803-806 (2001), USA.	
		"Combination Memories" searched on the Internet on April 28, 2003 <URL:http://www.sharp-world.com/products/device/flash/cmlist.html>	
		HWANG, Chorong-Lii et al, "A 2.9ns Random Access Cycle Embedded DRAM with a Destructive-Read Architecture", 2002 Symposium on VLSI Circuits Digest of Technical Papers, pp. 174-175, USA.	

Examiner Signature		Date Considered	
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